

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/528,751 Confirmation No. : 4687  
First Named Inventor : Markus ABERLE  
Filed : March 20, 2006  
TC/A.U. : 2863  
Examiner : Cosimano, E. R.

Docket No. : 102063.56895US  
Customer No. : 23911

Title : Circuit Configuration and Method for Identifying Error  
Situations in Inter Connected Systems

**Supplemental Response**

**Mail Stop AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following remarks are respectfully submitted to supplement the response previously filed on August 31, 2007 in the above-referenced application.

**Remarks** begin on page 2 of this paper.